



PATENT

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Date

April 5, 2004

Denise Sheridan

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Brian W. Huber and David R.
Brown

Attorney Docket No.: 500125.02

Serial No. : 10/007,871

Group Art Unit : 2818

Filed : November 13, 2001

Confirmation No. : 8896

Date of Notice
of Allowance : January 6, 2004

Title : METHOD OF SCALING DIGITAL CIRCUITS AND CONTROLLING THE TIMING
RELATIONSHIP BETWEEN DIGITAL CIRCUITS

FILING FORMAL DRAWINGS AFTER ALLOWANCE

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed are 4 sheets of formal drawings, Figures 1-10 for filing in the above-identified application.

Respectfully submitted,
DORSEY & WHITNEY LLP

Kimton N. Eng
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KNE:dms

Enclosures:

Postcard

Formal Drawings (Figures 1-10)

Copy of Notice of Allowance

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